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6/16/03

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	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	1062	361/42.ccls.	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 09:50
2	BRS	L2	347	11 and (circuit adj breaker)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 09:51
3	BRS	L3	123	12 and (trip adj circuit\$2)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 09:51
4	BRS	L4	19	13 and microprocessor	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 09:51
5	BRS	L5	8	14 and test	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 09:13

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6	BRS	L6	441	324/424.ccls.	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 09:50
7	BRS	L7	199	16 and (circuit adj breaker)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 09:51
8	BRS	L8	40	17 and (trip adj circuit\$2)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 09:51
9	BRS	L9	11	18 and microprocessor	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 09:52
10	BRS	L10	11	19 and microprocessor	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 10:02

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	Type	L #	Hits	Search Text	DBs	Time Stamp
11	BRS	L11	24	"4814712"	USPAT; US-PGP; UB; EPO; JPO; DERWEN; T; IBM_TD; B	2003/06/16 10:03

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	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	0	(circiut adj breaker) and (trip adj circuitry)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 07:09
2	BRS	L2	44	(circuit adj breaker) and (trip adj circuitry)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 07:12
3	BRS	L3	20	I2 and (current or voltage) adj source	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 07:37
4	BRS	L4	6	"4634981"	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 07:43
5	BRS	L5	13	"4803434"	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2003/06/16 07:51